

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	2823267
<b>Application Number:</b>	10690266
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	4584
<b>Title of Invention:</b>	Integrated circuit with a plurality of host processor family types
<b>First Named Inventor/Applicant Name:</b>	Dominik J. Schmidt
<b>Customer Number:</b>	21906
<b>Filer:</b>	Stephen James Curran/Deena Beasley
<b>Filer Authorized By:</b>	Stephen James Curran
<b>Attorney Docket Number:</b>	IVT.0033US
<b>Receipt Date:</b>	06-FEB-2008
<b>Filing Date:</b>	20-MAY-2004
<b>Time Stamp:</b>	16:33:25
<b>Application Type:</b>	Utility under 35 USC 111(a)

### Payment information:

Submitted with Payment	no
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed	US_IDS_Form__SB_08a.pdf	816996 33a5ffa64ae98b50e086d36dc59fd5b197ee57a5	no	4

### Warnings:

### Information:

2	NPL Documents	1-Digital_Planning_for_Single_Chip_Implementation_of_Digital_Wireless_Mobile_Transceiver.pdf	362062	no	5
			8bc2c0297b9982fa1bcb036402d017b7a34b16c6		
Warnings:					
Information:					
3	NPL Documents	2-Wide-Band_Direct_Conversion_Reciever_with_On-Chip_AD_Converters.pdf	216078	no	2
			36fef6e609a63e69bc1d25184cb494d0b27703cf		
Warnings:					
Information:					
4	NPL Documents	3-A_1-V19GHZ_CDMA_CMOSonSOL_Low_Noise_Amplifier.pdf	143292	no	2
			850118518a878fc723c3464635d18e98ce82e744		
Warnings:					
Information:					
5	NPL Documents	4-Mixed-Signal_Data_Conversion_Chip_for_Wireless_Communication_Systems.pdf	334926	no	5
			afea4e02f7f87b54aa42dfb10667ecb8c2ca490		
Warnings:					
Information:					
6	NPL Documents	5-Design_Methodology_Used_in_a_Single-ChipCMOS900MHZ_Spread-Spectrum_Wireless_transceiver.pdf	719512	no	6
			ada2249c2fefbd17a751442e232988e9058a3525		
Warnings:					
Information:					
7	NPL Documents	6-UTSICMOS_Technology_for_System-on-Chip_Solution.pdf	403156	no	6
			999a2031eb2513df088ed441cb174f748c674bf8		
Warnings:					
Information:					
8	NPL Documents	7-Digital_Signal_Processors_in_Cellular_Radio_Communications.pdf	3568307	no	14
			8bdbea6c74a9e028dd672094f19875b4f74ec3c5		
Warnings:					
Information:					
Total Files Size (in bytes):			6564329		

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**New Applications Under 35 U.S.C. 111**

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

**National Stage of an International Application under 35 U.S.C. 371**

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

**New International Application Filed with the USPTO as a Receiving Office**

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.